

This Page Is Inserted by IFW Operations
and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

IMAGES ARE BEST AVAILABLE COPY.

**As rescanning documents *will not* correct images,
please do not report the images to the
Image Problem Mailbox.**

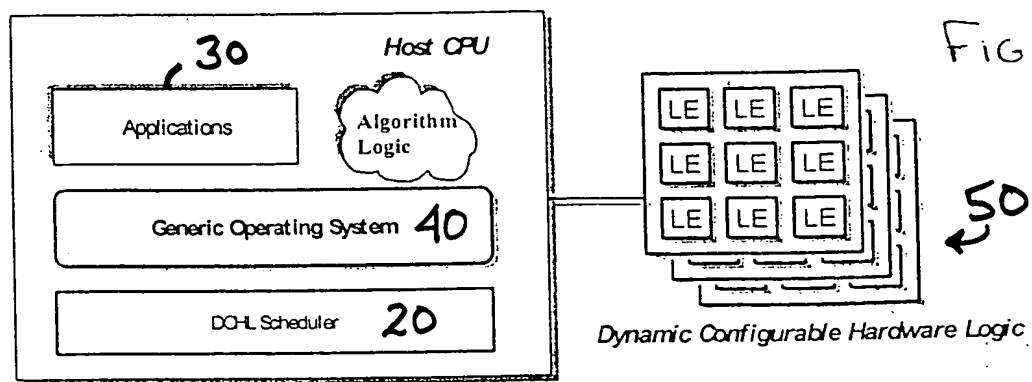
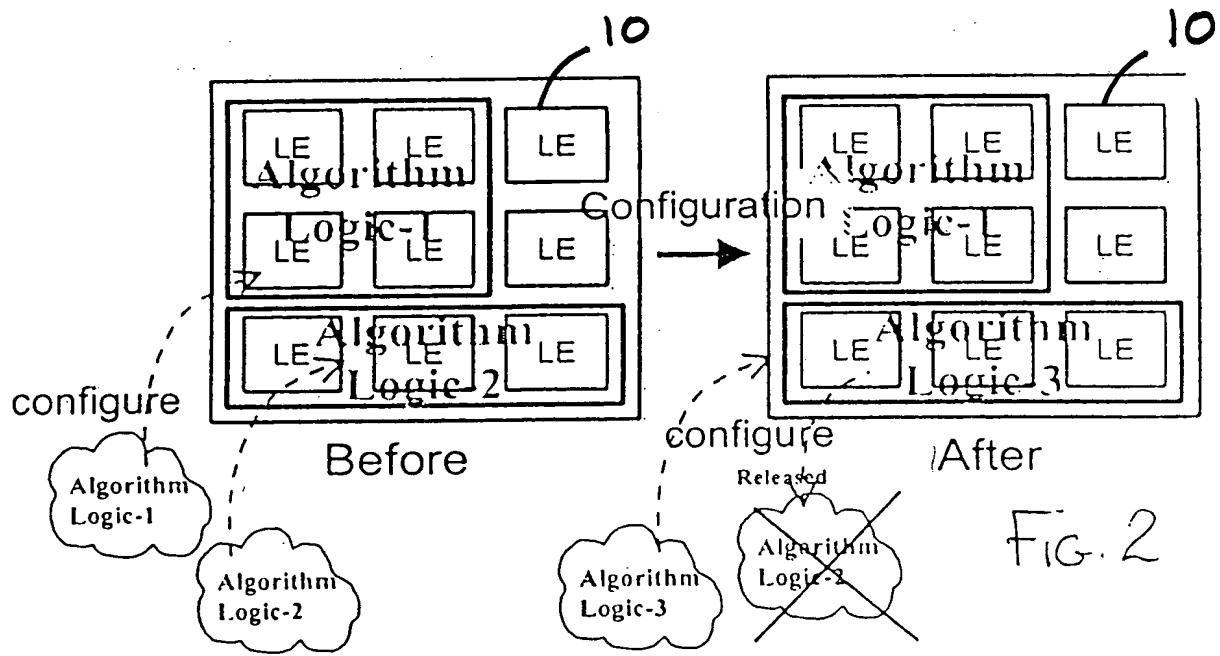
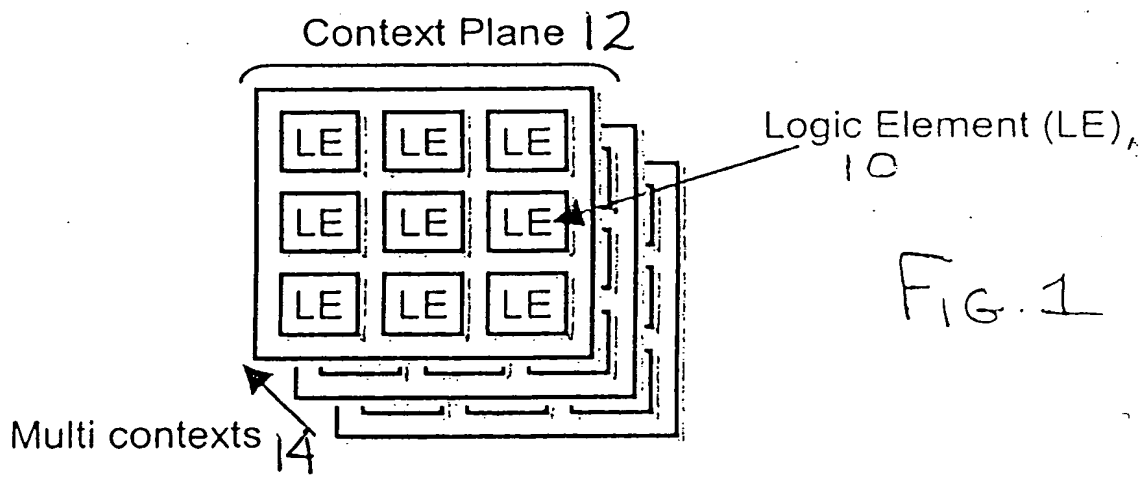


FIG. 4A

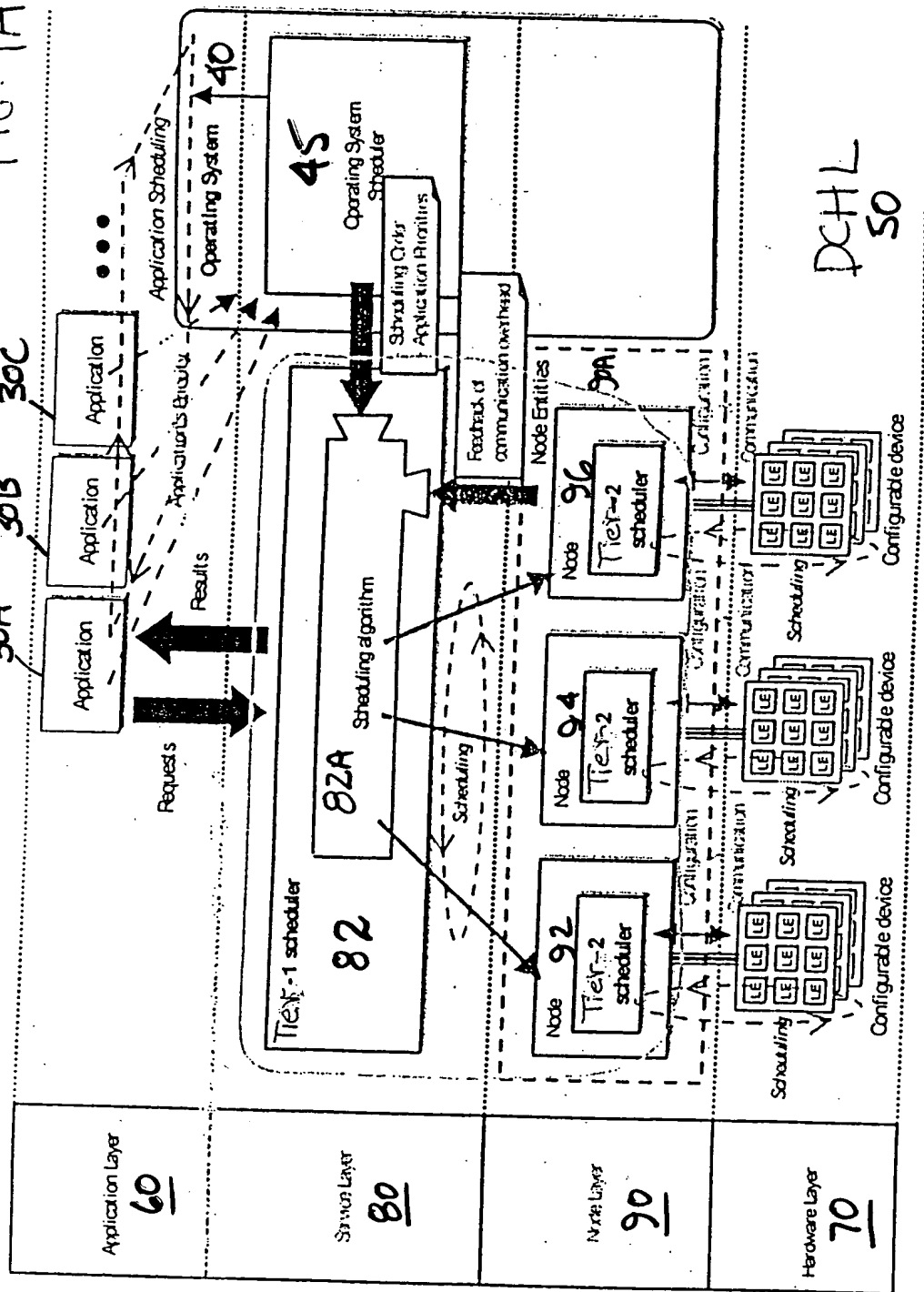


FIG. 4B

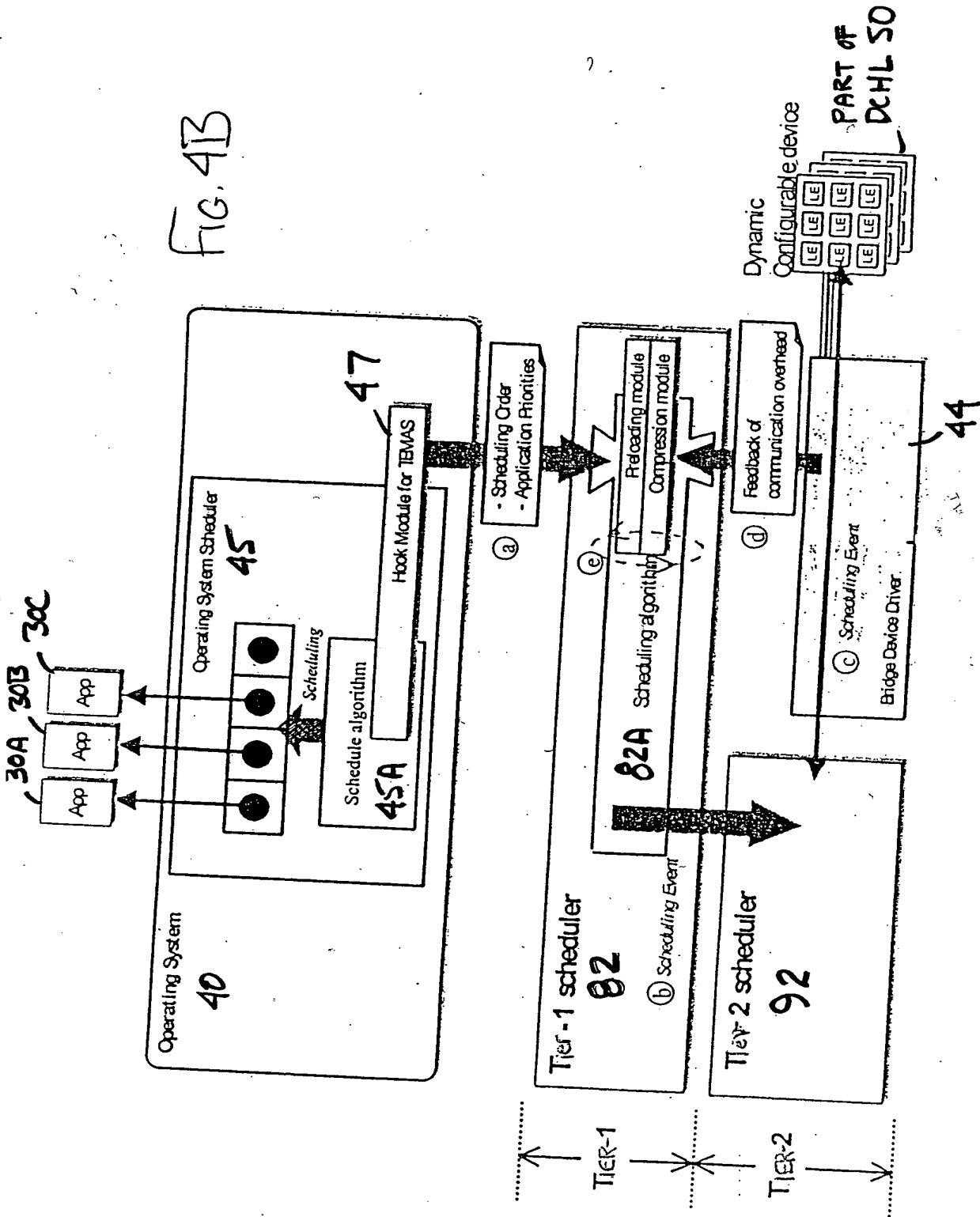


FIG. 5

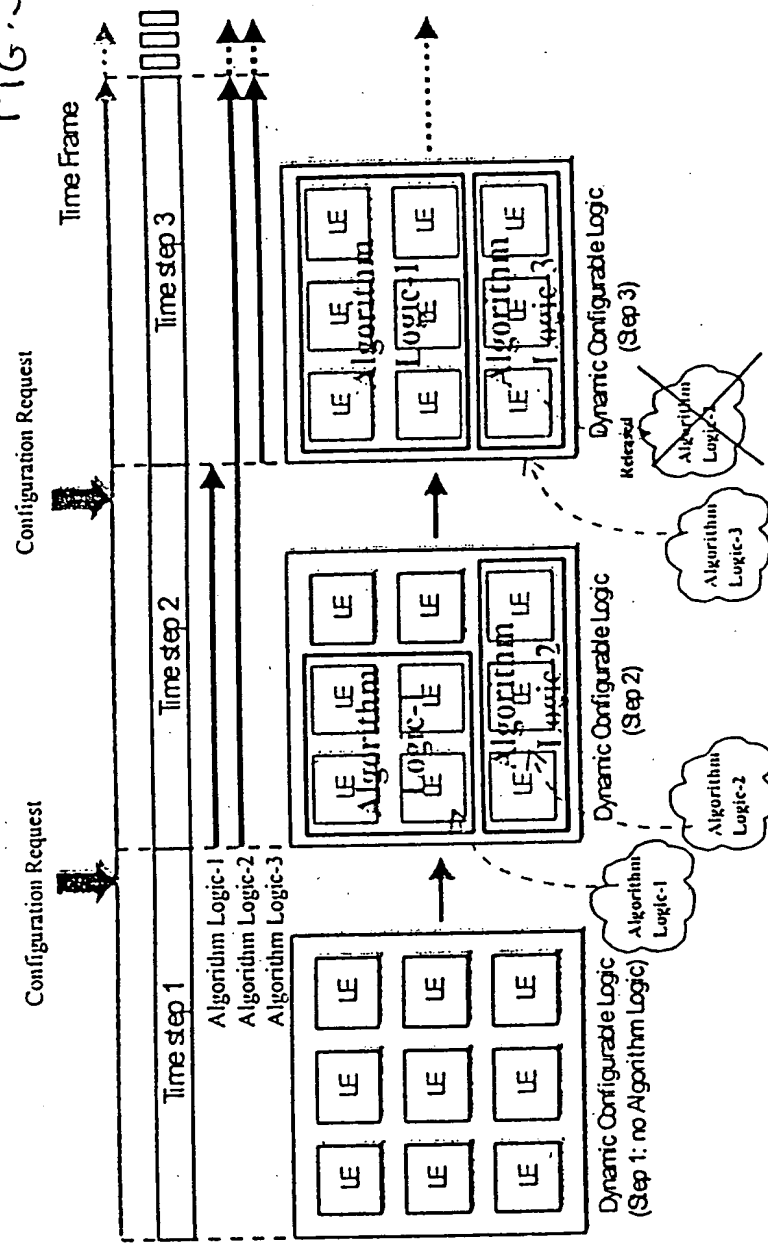


FIG. 6

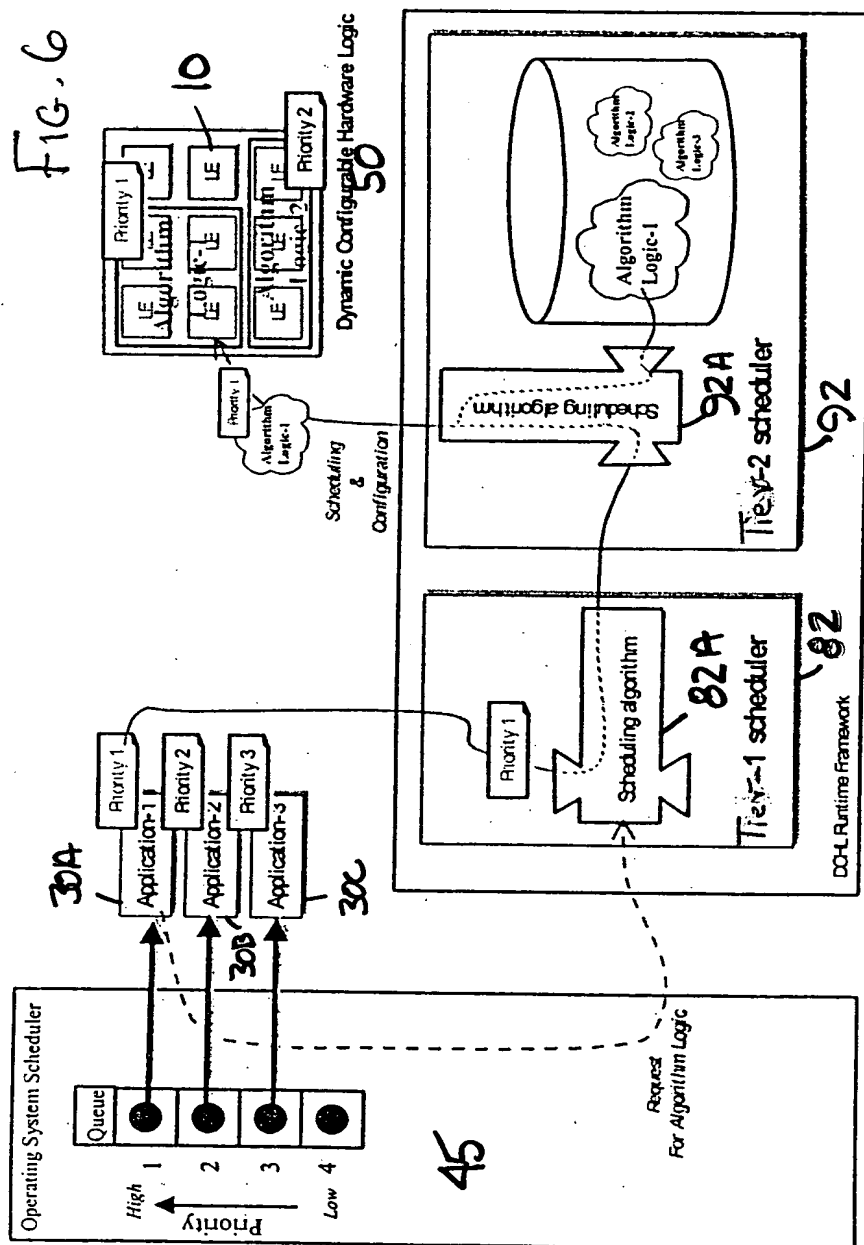
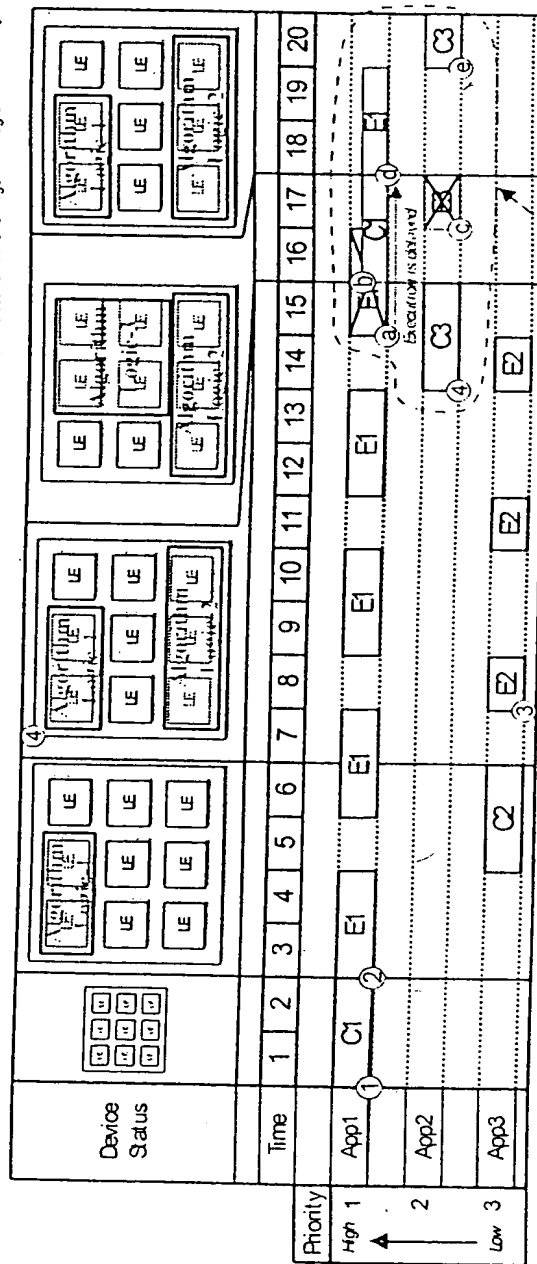


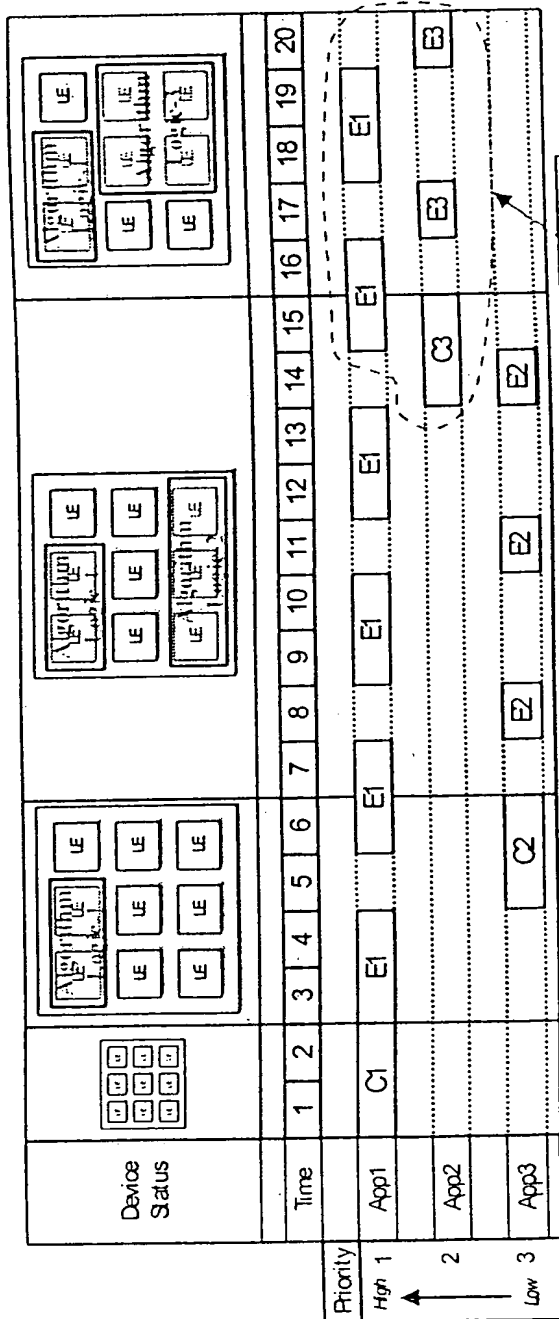
FIG. 7

(Conventions)
 App: Application working on Host CPU
 Cx: Configuration line for AlgorithmLogic x
 Ex: Execution line for AlgorithmLogic x



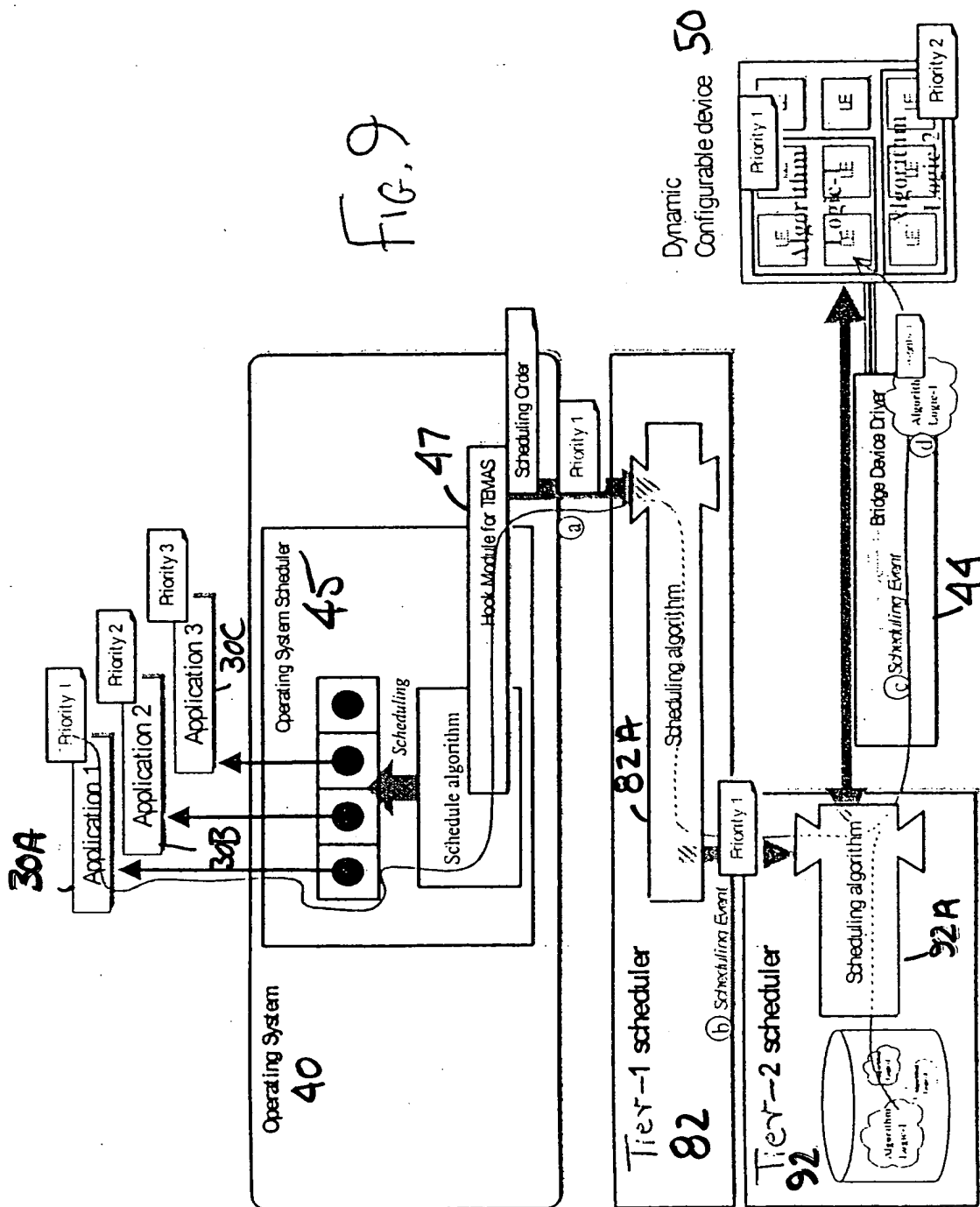
The Algorithm logic attached to application having the highest priority is delayed to work for from (a) to (d). Additionally, Algorithm-3 misses its execution at (c). Extra inefficient configuration both (b) and (e) are performed.

[Overlapped]
 App: Application working on I/O
 Cx: Configuration time for Algorithm Logic x
 E: Execution time for Algorithm Logic x



Algorithm logics attached to applications having higher priority are working fine, which means that execution delay and extra configuration doesn't occur. The PI ensures the best utilization.

FIG. 8



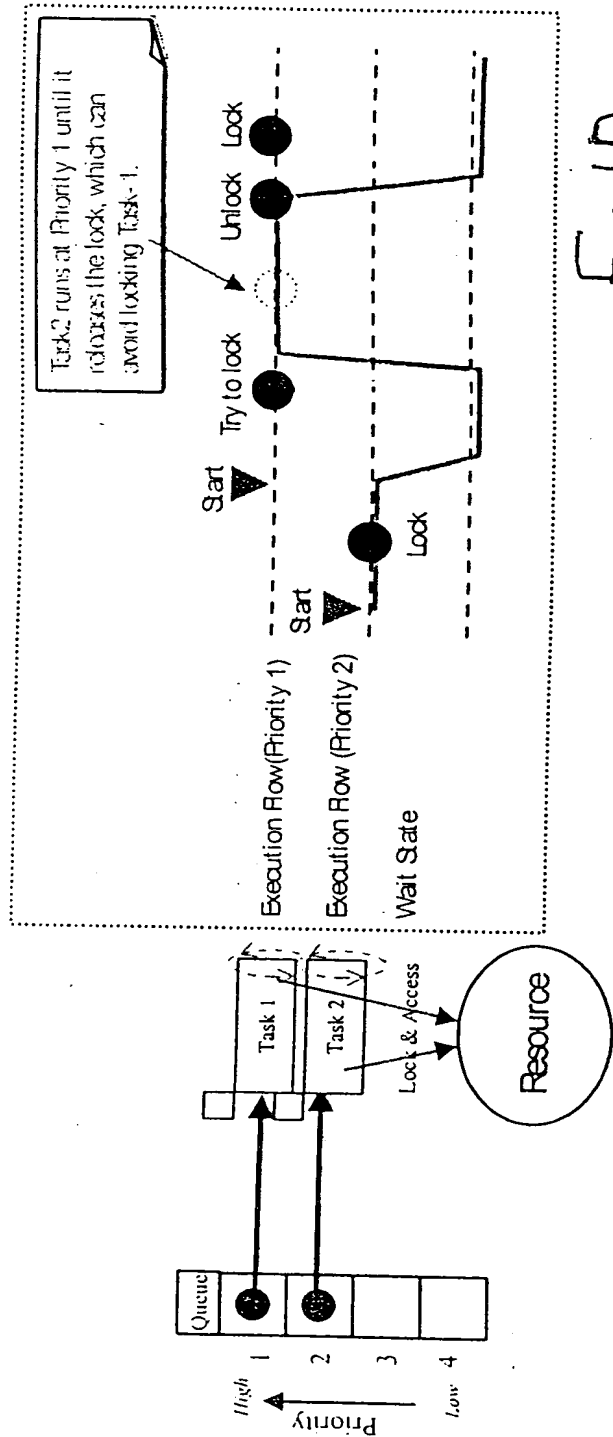


FIG. 10
PRIOR ART

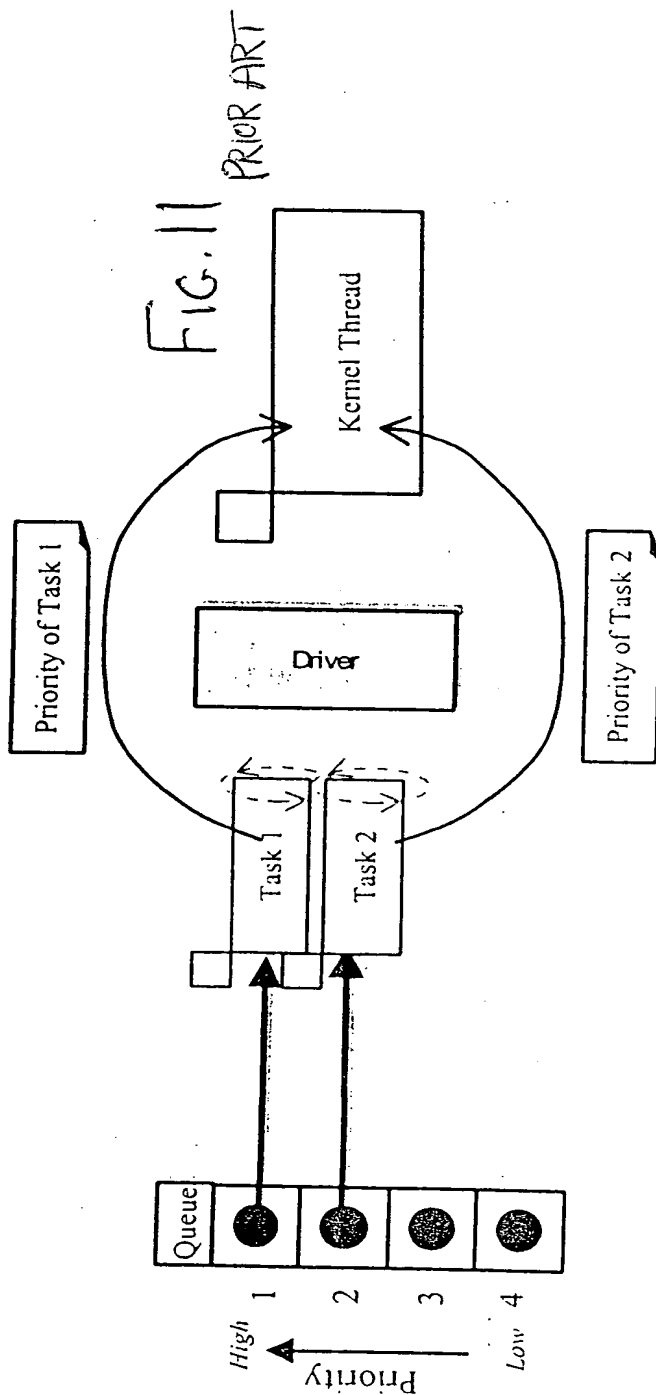


FIG. 11
PRIOR ART

FIG. 12
PRIOR ART

